Docket No.: GR 97 P 2681

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. ,20231 on the date indicated below:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor

Albrecht Mayer

Application No.

09/164,123

Filed

September 30, 1998

Title

Method for Assembling Integrated Circuits With Protection Of The Circuits Against Electrostatic Discharge, And Arragement Of Integrated Circuits With Protection Against Electrostatic

Discharge

Examiner

S. Clark

Art Unit:

2815

RESPONSE

Hon. Commissioner of Patents and Trademarks. Washington, D. C. 20231

Sir:

Responsive to the Office Action dated July 20, 1999, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group II, claims 1-11 for prosecution at this time. Applicants reserve the right to file a divisional application including the device claims of group I at a later date.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-11 is solicited.

Petition for extension is herewith made. The extension fee for response within a period of one (1) month pursuant to Section 1.136(a) in the amount of \$110.00 in accordance with Section 1.17 is enclosed herewith

Please charge any other fees which might be due with respect to Sections 1.16 and 1.17 to the Deposit Account of Herbert L. Lerner, No. 12-1099.

Respectfully submitted

09/02/1999 STEFERRA 00000165 09164123

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110.00 DP

For Applicant

Date: August 26, 1999 Lerner and Greenberg, P.A.

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SEP/1667 1999

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